

**AMENDMENTS TO THE ABSTRACT**

Please delete the Abstract Section of the specification and replace it with the Abstract attached hereto.

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Abstract of the Disclosure

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A circuit arrangement for a reception part of an SDH transmission system for transmitting plesiochronous signals contains input channels assigned to these signals which are connected to a clock synchronizer for the adaptation of the plesiochronous signals to a common processing clock. The clock synchronizer is followed by a multiplexer, to the output of which a reception processor is connected for the transformation of a plesiochronous signal into a synchronous signal for an SDH transmission channel. A circuit arrangement for a transmission part of the SDH transmission system contains a multiplexer, to the output of which a transmission processor is connected for the conversion of a transmitted synchronous signal into a plesiochronous signal. Using a desynchronizer following the transmission processor, the plesiochronous signal clocks are recovered, and the plesiochronous signals are issued to the output channels assigned to them.

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